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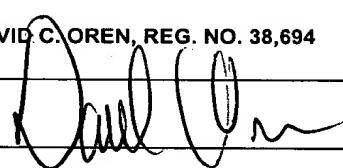
(to be used for all correspondence after initial filing)

		Application Number	09/708,450
		Filing Date	November 9, 2000
		First Named Inventor	Nakamura et al.
		Group Art Unit	2811
		Examiner Name	Douglas W. Owens
Total Number of Pages in This Submission	<>>	Attorney Docket Number	501.39149X00

### ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input checked="" type="checkbox"/> Supplemental Amendment	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
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<input type="checkbox"/> Certified Copy of Priority Documents	<input type="checkbox"/> CD, Number of CD(s) <<TEXT>>	
<input type="checkbox"/> Response to Missing Parts/Incomplete Application		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		
Remarks		

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual Name	DAVID C. OREN, REG. NO. 38,694
Signature	
Date	AUGUST 29, 2002

### CERTIFICATE OF MAILING

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Docket No.: 501.39149X00  
Serial No.: 09/708,450

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: NAKAMURA et al.

Serial No.: 09/708,450

Filed: November 9, 2000

For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND THE METHOD THEREOF

Group: 2811

Examiner: Douglas W. Owens

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SUPPLEMENTAL AMENDMENT

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

August 29, 2002

Sir:

As a supplement to the August 15<sup>th</sup> Amendment, please amend the above-  
identified application as follows:

IN THE CLAIMS:

Please add new claim 34 as follows:

*SUB-D17*  
--34. A method of producing a semiconductor integrated circuit device  
according to claim 29, wherein said first metal layer comprises a third metal layer  
formed by a sputtering method and a fourth metal layer formed by a chemical vapor  
phase growing method over said third metal layer.--

REMARKS

Claims 1-30 and 32-34 are pending in this application. By this Supplemental  
Amendment, claim 34 is added. Claim 34 corresponds to original claim 31 except  
that the "second metal layer" has been changed to "first metal layer". Support for

claim 34 is provided at least in paragraph [0080] of the substitute specification and Figures 8 and 10. In particular, the specification and drawings fully support a thin ruthenium film formed by sputtering prior to deposition of the ruthenium film by CVD. While applicants believe that the drawings show the claimed feature, applicants will add a further figure if desired by the Examiner. If the Examiner has any further questions or concerns concerning this claim or any other claim, then the Examiner is invited to contact applicants' undersigned attorney at (703) 312-6600.

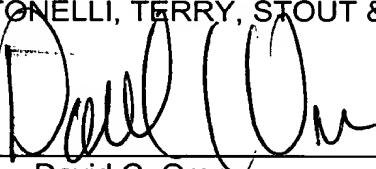
For the reasons set forth in the August 15<sup>th</sup> Amendment, all claims 1-30 and 32-34 are allowable.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 501.39149X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By

  
David C. Oren  
Registration No. 38,694

DCO/sjl